SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru

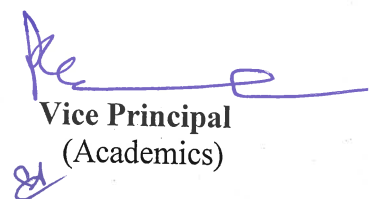## M.Tech I Semester (R20/R21) First Mid Term Examinations

TIME TABLE (Revised)

Timings: 02.00 p.m. to 3.30 p.m.   **10th December 2024**

Date	Structural Engineering	Power Electronics & Electric Drives	VLSI Design & Embedded Systems	CSE (Artificial Intelligence & Machine Learning) (R21 Regulation)
16.12.2024 (Monday)	Advanced Structural Analysis (CE3901)	Electrical Machine Modeling and Analysis (EE3901)	CMOS VLSI Design (EC4901)	Advanced Data Structures and Algorithm Analysis (CS4901)
17.12.2024 (Tuesday)	Theory of Elasticity (CE3902)	Analysis of Power Electronic Converters (EE3902)	Advanced Microcontrollers (EC4902)	Data Science (CS4902)
18.12.2024 (Wednesday)	Advanced R.C. Design (CE3903)	Renewable Energy Technology (EE3907)	Advanced Digital Design (EC4904)	Digital Image Processing (CS4903)
19.12.2024 (Thursday)	Advancements in Concrete Technology (CE3907)	Power Quality and Custom Power Devices (EE3904)	Parallel Processing (EC4908)	Internet of Things (CS4906)
20.12.2024 (Friday)	Research Methodology & IPR (BA3901)			
21.12.2024 (Saturday)	Constitution of India (BA3902)			

Note: Seating arrangement will be notified before the commencement of the examination by the respective departments.

Vice Principal

(Academics)

Copy to: HODs – CE/ECE/CSE/MBA/CoE

Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)